	Application No.	Applicant(s)
Notice of Allowability	10/808,318	TAKANO ET AL.
	Examiner	Art Unit
	David B. Lugo	2611
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>amendment filed 8/22/07</u> .		
2. The allowed claim(s) is/are 1,2 and 4-12.		
3.		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☑ Examiner's Amendr 8. ☐ Examiner's Stateme 9. ☐ Other	(PTO-413), te

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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

Claim 1, next to last line, "signal combination circuit" has been replaced with –signal combinational circuit--.

Priority

2. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Allowable Subject Matter

- 3. Claims 1, 2 and 4-12 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:

The prior art of record fails to disclose a communication semiconductor integrated circuit device as recited in claim 1 having a signal combinational circuit with a differential circuit and attenuation means, where the attenuated signal is provided between one differential input terminal and an external terminal, the other differential input terminal connected to another external terminal via elements identical to elements that constitute the attenuation means. The prior art of record further fails to teach a communication system as recited in claim 9 having attenuation means that sets the amount of attenuation of the detected signal in such a manner that when the power amplifier is operating at the minimum output level, the level of the detected

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signal input to the second external terminal becomes greater than the level of noise jumped from the first external terminal to the second external terminal. The prior art of record also fails to teach a communication semiconductor integrated device as recited in claim 12 having a signal combinational circuit including first and second attenuation means provided between the respective first and second differential input terminals of the signal combinational circuit and second and third external terminals, respectively, where the third external terminal is supplied with a predetermined potential.

Conclusion

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David B. Lugo whose telephone number is 571-272-3043. The examiner can normally be reached on M-F; 9:30-6.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Shuwang Liu can be reached on 571-272-3066. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent ... Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

David B. Lugo Primary Examiner

11/12/07